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U.S. PATENT & TRADEMARK OFFICE  
FEB 05 2001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

TI-30559

Laurence R. Simar, Jr., et al.

Serial No: 09/702,320

Filed: October 31, 2000

For: Microprocessor With Execution Packet Spanning Two or More Fetch  
Packets

**TRANSMITTAL OF DECLARATION UNDER 37 CFR 1.53(d)**

Ass't Commissioner for Patents

Washington, DC 20231

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)  
I hereby certify that the above correspondence is being deposited  
with the U.S. Postal Service as First Class Mail in an envelope  
addressed to: Ass't Commissioner for Patents, Washington, D.C.  
20231 on January 30, 2001.

  
Robin E. Barnum

In response to the *Notice of Missing Parts* dated January 11, 2001, enclosed  
herewith is an executed Declaration and Power of Attorney. Also enclosed is a copy of  
the *Notice to File Missing Parts of Application Form*.

Please charge the \$130 surcharge fee for filing of the Declaration Under 37  
CFR 1.16(e) to Deposit Account No. 20-0668. **This sheet is submitted in triplicate.**

Respectfully submitted,



Gerald E. Laws  
Attorney for Applicant  
Reg. No. 39,268

Texas Instruments Incorporated  
P.O. Box 655474, MS 3999  
Dallas, TX 75265  
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ATTORNEY'S DOCKET NO.

TI-30559

PAGE 1 OF 1

**APPLICATION FOR UNITED STATES PATENT**  
**DECLARATION AND POWER OF ATTORNEY**

As a below named inventor, I declare that my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor if only one name is listed below, or an original, first and joint inventor if plural inventors are named below, of the subject matter which is claimed and for which a patent is sought on the invention entitled as set forth below, which is described in the attached specification of **Application Serial No. 09/702,320, filed October 31, 2000**; that I have reviewed and understand the contents of the specification, including the claims, as amended by any amendment specifically referred to in the oath or declaration; that no application for patent or inventor's certificate on this invention has been filed by me or my legal representatives or assigns in any country foreign to the United States of America; and that I acknowledge my duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56;

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

TITLE OF INVENTION:  <b>Microprocessor With Execution Packet Spanning Two or More Fetch Packets</b>		
POWER OF ATTORNEY: I HEREBY APPOINT THE FOLLOWING ATTORNEYS TO PROSECUTE THIS APPLICATION AND TRANSACT ALL BUSINESS IN THE PATENT AND TRADEMARK OFFICE CONNECTED THEREWITH  <b>Practitioners at Customer Number: 23494</b>		
SEND CORRESPONDENCE TO:  Gerald E. Laws Texas Instruments Incorporated P.O. Box 655474, MS 3999 Dallas, TX 75265		DIRECT TELEPHONE CALLS TO:  Gerald E. Laws (972) 917-5287
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COUNTRY OF CITIZENSHIP:  US	COUNTRY OF CITIZENSHIP:  US	COUNTRY OF CITIZENSHIP:
SIGNATURE OF INVENTOR:  <i>X [Signature]</i>	SIGNATURE OF INVENTOR:  <i>X Richard Albi Brown</i>	SIGNATURE OF INVENTOR:
DATE:  <i>X 1/23/2001</i>	DATE:  <i>X 1/22/2001</i>	DATE: